Simple Control Scheme of Three-Level PWM Converter Connecting Wind Turbine with Grid

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Abstract. This paper proposes a simple direct power control using space vector modulation (DPC-SVM) for three-phase three-level neutral point clamped (3L-NPC) PWM converter connecting a wind turbine generator with grid. The active and reactive power are used as the control variables instead of the currents applied in conventional control method. Moreover, grid voltage sensors are replaced by a virtual flux (VF) estimator. The theoretical principle of this method as well as the synthesis of the active and reactive power controllers are discussed. It is shown that the proposed method exhibits several features as: simple and robust algorithm as well as simple tuning procedure of PI power controllers. Furthermore the paper presents a LCL-filter design, which fulfills IEEE-SCR 20-50 recommendation. Simulation results have proven an excellent performance and verified the validity of the proposed system.

Key words
control strategies, energy conversion, power quality, wind energy.

I. 1. Introduction

Generators and power electronics technology for wind turbines become one of the most promising technologies. Number of installed wind turbines increases every year rapidly and the necessity of grid-friendly interfaces application between grid and turbine is viable solution [1,2]. Modern, high performance PWM converter provide unity power factor and low harmonic distortion of current. It has a substantial influence for power quality, because non-sinusoidal currents delivered to the grid, can introduce an additional non-sinusoidal voltage drop across the line impedance and as a consequence, increase the grid voltage distortion, which is supplied to many other loads or could affect other generators. This publication will consider a new control method for three-level PWM converter connecting small power Permanent Magnet Synchronous Generator (PMSG) with the grid.

Various control strategies have been proposed in recent works on this type PWM converter [3,8]. A well-known method of indirect active and reactive power control is based on current vector orientation with respect to the line voltage vector [3,6-7]. This method guarantees an excellent dynamics and static performance via an internal current control loops. However, the final configuration and performance of the system largely depends on the quality of the applied current control strategy [4]. An other less known method based on instantaneous direct active and reactive power control is called Direct Power Control (DPC) [3]. Both mentioned strategies are sensitive for grid distortion, which reduces performance of the applied converter, because based on line voltage measurement. Only a DPC strategy based on virtual flux instead of the line voltage vector orientation, provides robust operation.

The rest of paper is organized as follows. Section II presents principles of virtual flux and power estimation as well as a basic block scheme of DPC-SVM. Moreover, this section is devoted to a presentation of synthesis of the power controllers and describes the influence of a coupling between active and reactive power. Section III shows a LCL-filter design. Sections IV and V contain results of a simulation study and final conclusions.

2. Control of PWM converter

Direct Power Control Space Vector Modulated (DPC-SVM) is based on the active and reactive power control loops [5,8-9,12]. In DPC-SVM there are no internal current control loops. Therefore, the key point of the DPC-SVM implementation is a correct and fast estimation of the active and reactive power (described in section A) as well as fast PI power controllers (described in sections B and C). A basic block scheme of the control method is shown in Fig. 1. The commanded reactive power \( q_{ref} \) (set to zero for unity power factor operation) and (delivered from the outer PI-DC voltage controller) the active power \( p_{ref} \) values are compared with the estimated \( q \) and \( p \) values, respectively. The errors are delivered to PI controllers, where the variables are DC quantities, what eliminates steady state error. The output signals from the PI controllers after a coordinate transformation from the synchronous rotating \( dq \) to stationary \( αβ \) reference frame are used for switching signals generation by Space Vector Modulator (SVM) [10].

A. Virtual Flux, Active and Reactive Power Estimator

One of the most effective methods to omit AC-line voltage sensors is the implementation of a virtual flux estimator. This simple noise resistant solution is based on the assumption that the integration of the voltages leads to an imaginary Virtual Fluxes (VF) [11,13]. Therefore, the following voltage equation:

\[
\mathbf{v} = \mathbf{r} + \mathbf{v}_f
\]

where \( \mathbf{v} \) is the grid line voltage, \( \mathbf{r} \) is the grid impedance and \( \mathbf{v}_f \) is the virtual flux vector. The virtual flux vector is defined as:

\[
\mathbf{v}_f = \int \mathbf{v}_d \, dt
\]

where \( \mathbf{v}_d \) is the virtual voltage vector. The virtual voltage vector is defined as:

\[
\mathbf{v}_d = \frac{1}{\omega_0} \left( \mathbf{v}_q - j \mathbf{v}_d \right)
\]

where \( \omega_0 \) is the grid line frequency and \( \mathbf{v}_q \) is the grid line voltage vector. The virtual flux vector is the integral of the virtual voltage vector and therefore:

\[
\mathbf{v}_f = \frac{1}{\omega_0} \int \left( \mathbf{v}_q - j \mathbf{v}_d \right) \, dt
\]

This equation can be written in matrix form as:

\[
\mathbf{v}_f = \frac{1}{\omega_0} \left( \int \mathbf{v}_q \, dt \right) - j \mathbf{v}_d
\]

where \( \mathbf{v}_f \) is the virtual flux vector, \( \mathbf{v}_q \) is the grid line voltage vector and \( \mathbf{v}_d \) is the virtual voltage vector. The virtual flux vector is the integral of the virtual voltage vector and therefore:

\[
\mathbf{v}_f = \frac{1}{\omega_0} \left( \int \mathbf{v}_q \, dt \right) - j \mathbf{v}_d
\]
\[ u_G = u_c + u_R = L \frac{di_G}{dt} + u_R \quad (1) \]

can be replaced by the virtual flux equation [13], see Fig. 2:
\[ \psi = \psi_s + \psi_L \quad (2) \]

Using a complex notation, the instantaneous power can be calculated as follows:

\[ p = \text{Re}(u_G \cdot i_G^*) \quad (3a) \]
\[ q = \text{Im}(u_G \cdot i_G^*) \quad (3b) \]

where \( i_G^* \) denotes the conjugate current vector. The grid voltage can be expressed by the virtual flux as
\[ u_G = \frac{d}{dt} \psi_G + j \omega \psi_G + j \omega \psi_G \quad (4) \]

where \( \psi_G \) denotes the space vector and \( \psi_G \) its amplitude. For virtual flux-oriented quantities, in \( \alpha-\beta \) coordinates (Fig. 2) and using (3) and (4)
\[ u_G = \frac{d}{dt} \psi_G + j \omega \psi_G + j \omega \psi_G \quad (5) \]

That gives
\[ p = \frac{d}{dt} \psi_G \left| i_{\alpha} \right|^2 + \omega \left( \psi_G \cdot \left( \psi_{\alpha} \psi_{\beta} \right) \right) \quad (6a) \]
\[ q = \frac{d}{dt} \psi_G \left| i_{\beta} \right|^2 + \omega \left( \psi_G \cdot \left( \psi_{\alpha} \psi_{\beta} \right) \right) \quad (6b) \]

For sinusoidal and balanced voltages the derivatives of the flux amplitudes are zero. The instantaneous active and reactive powers can be computed as
\[ p = \omega \cdot \left( \psi_{\alpha} i_{\beta} - \psi_{\beta} i_{\alpha} \right) \quad (8a) \]
\[ q = \omega \cdot \left( \psi_{\alpha} i_{\alpha} + \psi_{\beta} i_{\beta} \right) \quad (8b) \]

The full estimation process of virtual flux and active and reactive power estimation is presented in Fig. 3.
B. Synthesis of Active and Reactive Power Controllers

The synthesis of active and reactive power controllers can be done analytically using a simplified model. In this model the switching waveforms created by the PWM converter are replaced by its average value within the switching period [9].

The model in dq coordinates has the form:

\[ u_{gd} = R_l i_{gd} + L \frac{di_{gd}}{dt} - \omega L i_{gq} + u_{gd} \]
\[ u_{gq} = R_l i_{gq} + L \frac{di_{gq}}{dt} + \omega L i_{gd} + u_{gq} \]

(9)

With the reference frame defined in Fig. 2 the equations

\[ u_{gd} = U \]
\[ u_{gq} = 0 \]

(10)

and

\[ p = U i_{gq} \]
\[ q = U i_{gd} \]

(11)

can be described. Introducing (10) into (9) the model simplifies to:

\[ 0 = R_l i_{gd} + L \frac{di_{gd}}{dt} - \omega L i_{gq} + u_{gd} \]
\[ U = R_l i_{gq} + L \frac{di_{gq}}{dt} + \omega L i_{gd} + u_{gq} \]

(12)

Introducing PI controllers for active power \( p \) and reactive power \( q \) the block diagram in Fig. 4 is obtained.

The active and reactive power controllers are coupled by the cross terms. The synthesis of the PI parameters should be performed in order to get a good response and to minimize coupling effects.

Considering the reactive power null, that is \( i_{d0} = 0 \), the active power control loop becomes disconnected from the reactive power (the influence of the reactive power is analysed later). The block diagram for the active power control loop is shown in Fig. 5.

Figure 5: Active power control block diagram

The line voltage is seen as a constant perturbation, and should be compensated by the integral part of the PI controller.

In this way, the zero of the PI controller is placed over the pole of the system. So, the equation

\[ T_n = \frac{T_i}{R} = T_{cl} \]

(13)

follows, where \( T_{cl} \) is the system's open loop time constant. With this synthesis, in closed loop, the system will be shown as in Fig.6:

Figure 6: Closed loop block diagram

The closed loop transfer function is:

\[ \frac{1}{1 + G_{eq}} = \frac{U}{1 + \frac{s T_i R}{U}} = \frac{1}{1 + \frac{s T_i R}{U}} \]

(14)

The closed loop time constant \( T_{cl} \) is given by:

\[ T_{cl} = \frac{T_i R}{U} \]

(15)

and can be a specification for the controller design. So

\[ T_i = \frac{U T_{cl}}{R} \]

(16)

The parameters of the PI controller can be given by [9]:

\[ k_p = \frac{T_n}{T_i - \frac{L}{U T_{cl}}} \]
\[ k_i = \frac{1}{T_i - \frac{R}{U T_{cl}}} \]

(17)
The specification of \( T_{cl} \) should be done in order to get a good response and decoupling between both controllers. The ratio of \( k_p/k_i \) for different closed loop time constants \( T_{cl} \) is constant and equal to an open loop time constant \( T_{ol} \). Because the active and reactive power control loops are similar, equations (17) are valid for both controllers.

C. Influence of Coupling Between the d and q Axis

Considering now a perturbation \( y \) on the active power control loop as shown in Fig. 7 [9],

\[
\begin{align*}
p^* & \quad + \quad \frac{1+sT_{cl}}{sT_i} + \quad y \quad \frac{U}{R+Ls} \quad + \quad p \\
\end{align*}
\]

Figure 7: Simplified block diagram

On the active power control loop, equation (12), the perturbation is given by:

\[ y = -\omega L i_{Ld} = -\omega L \frac{q}{U} \] (18)

Using the model of Fig. 7 it is possible to determine analytically the influence of the reactive power variation on the active power. Using Laplace transforms the effects of the perturbation are determined by:

\[ \Delta P(s) = \frac{-s}{s + \frac{1}{T_{cl}}} \frac{\omega Q(s)}{s + \frac{1}{T_{cl}}} \] (19)

The inverse Laplace transform gives [9]:

\[ \Delta p(t) = -\frac{\omega T_{cl} q_0}{1 - \frac{T_{cl}}{T_{ol}}} \left[ e^{-\frac{t}{T_{cl}}} - e^{-\frac{t}{T_{ol}}} \right] \] (20)

Equation (20) shows that a step variation on the reactive power of a value \( q_0 \) produces an undesirable variation on the active power that is approximately given by:

\[ \Delta p(t) = -\omega T_{cl} q_0 \] (21)

Note that this equation is approximately valid because the closed loop time constant is smaller than the open loop time constant. It can be concluded also that the elimination of the perturbation is determined by the open loop time constant. This is a drawback of this synthesis that can be corrected by adopting small values of the closed loop time constant.

For the reactive power control loop it is easy to conclude that a similar formula can be obtained. Thus equation (22) is obtained.

\[ \Delta q(t) = \frac{\omega T_{cl} P_0}{1 - \frac{T_{cl}}{T_{ol}}} \left[ e^{-\frac{t}{T_{cl}}} - e^{-\frac{t}{T_{ol}}} \right] \] (22)

Similarly to eq. 21 the eq. 22 can be reduced to the form:

\[ \Delta q(t) = \omega T_{cl} P_0 \] (23)

3. LCL filter design

The reduction of the current harmonics around switching frequency and multiplication of switching frequency is done to achieve compliance with grid codes as IEEE 519-1992 [15]. High inductance values can achieve this goal, however, inductor is bulky, expensive and will limit the converter dynamics as well as the total operating range. The voltage drop across the inductance is controlled by the fundamental component of the PWM converter voltage with its maximal amplitude limited by the DC-link voltage. Consequently, a high current through the inductance requires either a high dc-link voltage or a low inductance. The maximal inductance can be determinate to [3,12]:

\[
L < \sqrt{\frac{u_d^2}{3} - \left( \frac{U_{gm}}{\omega I_m} \right)^2}
\] (24)

where \( u_d \) – dc-link voltage, \( U_{gm} \) – amplitude of grid voltage, \( I_m \) – amplitude of current, \( \omega \) - angular frequency of the grid.

The application of a LCL low pass filter (Fig. 8) with a relatively small filter size and a good decoupling of the filter performance from grid impedance variations provides a good performance. From Fig. 9a and Fig. 9b it can be seen that the ac filter can be smaller / less heavy for a three-level topology for any desired current ripple value or harmonic spectrum. Due to the quadratic dependency of ripple current and losses the application of a three-level converter will result in substantially smaller losses in any given inductance.

![Diagram of a three-phase LCL-filter connected between grid and 3L-NPC PWM rectifier](image)
b) Fig. 12 Harmonic spectrum of the line-to-line voltage for 
a) two-level converter b) three-level converter

For the filter design a variety of optimization criteria exist such as minimum cost, losses, volume and weight. Selection was chosen to fulfill IEEE-SCR 20-50 recommendation [15], where SCR describe short-circuit ratio of the bus capacity to the ratings of the converter in MVA. The analysis was done in Matlab/Simulink with resonance frequency around 0.5\( f_s \). The transfer functions are given by:

\[
H_1(s) = \frac{i_G(s)}{u_G(s)} = \frac{L_1C s^2 + 1}{L_1L_2C s^3 + (L_1 + L_2)s} \tag{25}
\]

and

\[
H_2(s) = \frac{i_G(s)}{u_G(s)} = \frac{1}{L_1L_2C s^3 + (L_1 + L_2)s} \tag{26}
\]

Based on Eq. 24 and Table 1 the total inductance of the LCL filter should be less then 40mH (0.86 in per unit notification of the base impedance \( z_B = (U_{L-L})^2/S_0 \)). However to achieve good dynamics, reasonable costs, weight and losses, the inductance should be around or less then 0.1 pu in real realization. For the IEEE 519-1992 limitations and the assumption that \( \Delta I_{\text{ripple,peak}} \) not exceeds 10% (2.3A) the converter side, inductance \( L_1 \), implemented with iron powder core, can be calculated to be [16]:

\[
L_1 = \frac{U_G}{2 \sqrt{6 f_s \Delta I_{\text{ripple,peak}}}} \tag{27}
\]

Typically \( L_1 \) is larger than the line side inductance \( L_2 \), which is implemented as iron steel core, in order to attenuate most of the current ripple. In this case a split factor \( L_2/L_1 \) of 0.8 was chosen to obtain a ripple attenuation of 80%. In order to keep high power factor, the capacitor is limited to 5% of the reactive power absorbed at rated conditions [14]. Thus the capacitance of the LCL filter (Y-connected) should not be larger than:

\[
C \leq \frac{P_e}{120 \pi f_s U_G^2} \tag{28}
\]

The values of parameters used in simulation as well as the LCL-filter components, for PWM converter at 8kHz switching frequency, in order to attenuate the switching harmonics below IEEE-SCR 20-50 limits, are given in Table 1.

<table>
<thead>
<tr>
<th>Table 1 Parameters used in simulation</th>
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<tbody>
<tr>
<td>Parameter</td>
</tr>
<tr>
<td>Power</td>
</tr>
<tr>
<td>Grid voltage</td>
</tr>
<tr>
<td>DC link voltage</td>
</tr>
<tr>
<td>Current</td>
</tr>
<tr>
<td>Switching frequency</td>
</tr>
<tr>
<td>Grid frequency</td>
</tr>
<tr>
<td>Converter side inductor in LCL-filter</td>
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<tr>
<td>Grid side inductor in LCL-filter</td>
</tr>
<tr>
<td>Capacitor in LCL-filter</td>
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4. Results

To study the operation of the DPC-SVM system, the PWM converter including control scheme has been simulated using the MATLAB/SIMULINK and SABER. The main electrical parameters of the power circuit and control data are given in the Table 1. The simulation study has been performed with two main objectives in mind:
- the steady state operation (Fig. 10) and,
- the operation of the LCL-filter (Fig. 11);

The achieved results shown an excellent performance and verify the validity of the proposed system.

Fig. 10. Simulated basic signal waveforms for DPC-SVM:
From the top: grid voltage \( u_G \), current \( i_G \), instantaneous active \( p \) and reactive power \( q \), 3L-NPC converter voltage.
5. Conclusion

A Direct Power Control Space Vector Modulated scheme for three-phase PWM converter systems constitutes a viable alternative to conventional control strategies. It offers the following features and advantages:

- No line voltage sensors are required;
- Noise resistant and low sampling frequency control algorithm;
- Simple PI power regulators tuning procedure, which provides a decoupling between the active and reactive power enables an excellent performance and dynamic;

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References