

Sensorless AC Current Control with Backstepping Design for a PWM AC-DC Converter

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Abstract. In this paper, a novel sensorless AC current control scheme is proposed for the design of PWM AC-DC converter. This design strategy deals with nonlinear backstepping controllers and AC current observer to achieve the purpose of current following. In general, PWM AC-DC converter design requires AC current measurement to achieve control goals, the backstepping observer employed estimates the AC current, and then nonlinear controllers based on backstepping design algorithm are developed to realize the sensorless AC current for the PWM AC-DC converter system. The proposed control scheme is not only to stabilize the PWM AC-DC converter system, but also to drive the current tracking error to converge to zero asymptotically. Furthermore, some simulation results are shown to illustrate excellent performances of the nonlinear backstepping control design scheme applied to a PWM AC-DC converter.

Key words: Backstepping control; Backstepping observer; PWM AC-DC converter; sensorless.

1. Introduction

Three phase AC-DC converters are widely used in industrial applications. Static power converters are nonlinear in nature and consequently generate harmonics into the supply. As a result, the power factor of the converters is usually poor and varies with the load. Most electrical systems are designed on the fundamental frequency [10].

Many research results focusing on the control of AC-DC converters have been reported [1]-[10] such as the adaptive backstepping controller and the feedback linearization technique applied to control the output voltage of three phase AC-DC PWM converter [1]-[2] with and without LCL input filters [7].

Different nonlinear techniques were also applied to converters in electric power networks such as DC/DC series resonant converter and three-phase three-level neutral point clamped rectifier [4]-[5]-[6].

Nonlinear backstepping control [8]-[9]-[11] is a newly developed systematic design method. The most appealing point of it is to use the virtual control variable to make

the high-order system simple and thus the final control outputs can be derived step by step through appropriate Lyapunov functions.

The design of PWM AC-DC converter systems, in general, requires input current for AC-DC converter measurement to achieve control targets, but lots of PWM AC-DC converter systems do not have AC current [3]-[12]-[13] or AC voltage [14]-[15] or DC voltage measurement devices. Eliminating AC current or AC or DC voltage sensors simplifies the structure of the driver system.

In this paper, with the proposed backstepping AC current observer, a nonlinear backstepping control design scheme is developed for the current tracking control of PWM AC-DC converter that has exact model knowledge. The asymptotic stability of the resulting closed-loop system is guaranteed according to Lyapunov stability theorem. As a result, the proposed nonlinear backstepping control design without the use of current sensor is not only to stabilize the AC-DC converter system, but also to force the current tracking error to converge to zero asymptotically.

The remainder of this paper is organized as follows. In section 2, the dynamic model of a PWM AC-DC converter is introduced with some important system properties. The sensorless backstepping control scheme consisting of an AC current observer and current input controllers are developed in Section 3 for the purpose current tracking. The simulation results are illustrated in Section 4, and some concluding remarks are given in Section 5.

2. Mathematical model of PWM AC-DC converter

Figure 1 represents the topology of the converter under study. The dynamic model of a PWM AC-DC converter can be described in the well known (d,q) frame through the Park transformation [1]-[2] as follows:

$$\begin{aligned} \dot{i}_d &= -\frac{R}{L}i_d + \omega i_q + \frac{1}{L}(E_d - v_d) \\ \dot{i}_q &= -\frac{R}{L}i_q - \omega i_d + \frac{1}{L}(E_q - v_q) \\ \dot{v}_{dc} &= \frac{2}{3Cv_{dc}}(E_d i_d + E_q i_q) - \frac{v_{dc}}{CR_L} \end{aligned} \quad (1)$$

Where i_d and i_q are (d, q) axis currents, E_d and E_q are (d, q) axis source voltage, v_{dc} is the DC output voltage, v_d and v_q are (d, q) axis converter input voltage. R and L mean the line resistance and inductance, respectively, R_L is the load resistance.

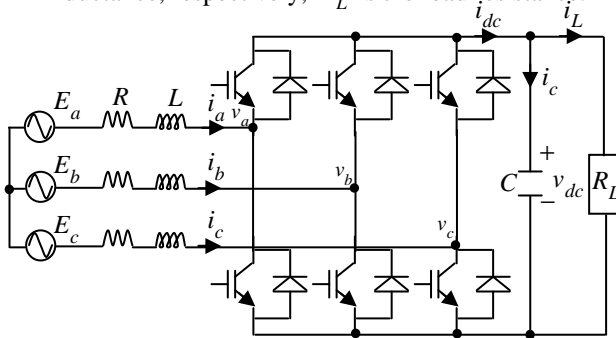


Fig. 1 AC/DC PWM converter topology

3. Sensorless backstepping control

Since the current measurement of the input AC-DC converter is usually unavailable, an AC current observer must be employed to estimate the actual AC current. Hence, the AC current estimation error is defined by:

$$\begin{aligned} \tilde{i}_d &= i_d - \hat{i}_d \\ \tilde{i}_q &= i_q - \hat{i}_q \end{aligned}$$

Where \hat{i}_d is the estimate of i_d and \hat{i}_q is the estimate of i_q .

Now, we employ backstepping schemes to design the nonlinear controllers with the AC current observer, the backstepping design procedure consists of three steps:

Step 1: First of all, since the current i_d must be forced to be zero, the first regulated variable is selected as

$$e_d = i_d \quad (2)$$

The derivative of (2) is computed as

$$\dot{e}_d = \dot{i}_d = -\frac{R}{L}i_d + \omega i_q + \frac{1}{L}E_d - \frac{v_d}{L} \quad (3)$$

The first Lyapunov candidate V_1 is chosen as

$$V_1 = \frac{1}{2}e_d^2 \quad (4)$$

The derivative of (4) is computed as

$$\begin{aligned} \dot{V}_1 &= e_d \dot{e}_d \\ \dot{V}_1 &= -k_1 e_d^2 + e_d \left(k_1 e_d - \frac{R}{L}i_d + \omega(\tilde{i}_q + \hat{i}_q) + \frac{E_d}{L} - \frac{v_d}{L} \right) \end{aligned} \quad (5)$$

At this point, the d -axis voltage control input v_d can be selected by

$$v_d = L \left(k_1 e_d + \omega \hat{i}_q \right) + E_d \quad (6)$$

Where k_1 is a positive design constant, so (5) becomes

$$\dot{V}_1 = -\left(k_1 + \frac{R}{L} \right) e_d^2 + \omega \tilde{i}_q e_d \quad (7)$$

Step 2: the purpose of this control design is to achieve the reference voltage tracking, so the second regulated variable is denoted by

$$e = v_{dc} - v_{dcref} \quad (8)$$

Where v_{dcref} is the reference signal of DC voltage.

Hence, the derivative of (8) is calculated as

$$\dot{e} = \frac{2}{3Cv_{dc}} E_d i_d + \frac{2}{3Cv_{dc}} E_q i_q - \frac{v_{dc}}{CR_L} - \dot{v}_{dcref} \quad (9)$$

By defining the error variable $e_q = i_q - i_{qref}$, where i_{qref} is the stabilizing function chosen as follows:

$$i_{qref} = \frac{1}{E_q} \left(-E_d i_d + \frac{3Cv_{dcref}}{2} \left(\frac{v_{dcref}}{CR_L} + \dot{v}_{dcref} \right) \right) \quad (10)$$

(9) can be rewritten as

$$\begin{aligned} \dot{e} &= \frac{2}{3Cv_{dc}} E_q e_q + \frac{2}{3Cv_{dc}} E_q i_{qref} + \frac{2}{3Cv_{dc}} E_d i_d \\ &\quad - \frac{v_{dc}}{CR_L} - \dot{v}_{dcref} \\ \dot{e} &= -\frac{1}{CR_L} e + \frac{2}{3Cv_{dc}} E_q e_q \end{aligned} \quad (11)$$

With the choice of the second Lyapunov candidate

$$V_2 = \frac{1}{2}k_2 e^2 \quad (12)$$

Where k_2 a positive design constant, the derivative of (12) is computed as

$$\dot{V}_2 = k_2 e \dot{e}$$

$$\dot{V}_2 = -\frac{k_2}{CR_L} e^2 + \frac{2k_2}{3Cv_{dc}} E_q e_q e \quad (13)$$

Step 3: The derivative of the given error variable e_q is computed as

$$\begin{aligned} \dot{e}_q &= \dot{i}_q - \dot{i}_{qref} \\ \dot{e}_q &= -\frac{R}{L}i_q - \omega i_d + \frac{E_q}{L} - \frac{v_q}{L} + \frac{E_d}{E_q} \frac{de_d}{dt} - \frac{3}{E_q R_L} v_{dcref} \dot{v}_{dcref} \\ &\quad - \frac{3C}{2E_q} \dot{v}_{dcref}^2 - \frac{3C}{2E_q} v_{dcref} \ddot{v}_{dcref} \end{aligned} \quad (14)$$

Therefore, by following the choice of (4) and (12), the complete Lyapunov function candidate is selected as

$$V = V_1 + V_2 + V_3 + \frac{1}{2\gamma_1} \tilde{i}_d^2 + \frac{1}{2\gamma_2} \tilde{i}_q^2, \quad (15)$$

Where γ_1, γ_2 are the positive adaptation gains. From (7) and (13), (14), the derivative of (15) is computed as follows;

$$\dot{V} = \dot{V}_1 + \dot{V}_2 + \dot{V}_3 + \frac{1}{\gamma_1} \tilde{i}_d \dot{\tilde{i}}_d + \frac{1}{\gamma_2} \tilde{i}_q \dot{\tilde{i}}_q \quad (16)$$

$$\begin{aligned} \dot{V} = & - \left(k_1 + \frac{R}{L} \right) e_d^2 - \frac{k_2}{CR_L} e^2 - k_3 e_q^2 + \frac{1}{\gamma_1} \tilde{i}_d \dot{\tilde{i}}_d + \frac{1}{\gamma_2} \tilde{i}_q \dot{\tilde{i}}_q \\ & + e_q \left(k_3 e_q - \frac{R}{L} \hat{i}_q - \omega \hat{i}_d + \frac{E_q}{L} \frac{v_q}{L} + \frac{E_d}{E_q} \frac{de_d}{dt} \right. \\ & \left. - \frac{3}{E_q R_L} v_{dref} \dot{v}_{dref} - \frac{3C}{2E_q} \dot{v}_{dref}^2 - \frac{3C}{2E_q} v_{dref} \ddot{v}_{dref} \right) \\ & + \omega \tilde{i}_q e_d + \frac{2k_2}{3Cv_{dc}} E_q e_q e \\ \dot{V} = & - \left(k_1 + \frac{R}{L} \right) e_d^2 - \frac{k_2}{CR_L} e^2 - k_3 e_q^2 - \frac{R}{\gamma_1 L} \tilde{i}_d^2 - \frac{R}{\gamma_2 L} \tilde{i}_q^2 \\ & + e_q \left(k_3 e_q - \frac{R}{L} \hat{i}_q - \omega \hat{i}_d + \frac{E_q}{L} \frac{v_q}{L} + \frac{2k_2}{3Cv_{dc}} E_q e \right. \\ & + \frac{E_d}{E_q} \left(-\frac{R}{L} \hat{i}_d + \omega \hat{i}_q + \frac{E_d}{L} \frac{v_d}{L} \right) + \frac{E_q}{L} \frac{v_q}{L} - \frac{3}{E_q R_L} v_{dref} \dot{v}_{dref} - \frac{3C}{2E_q} \dot{v}_{dref}^2 \\ & \left. - \frac{3C}{2E_q} v_{dref} \ddot{v}_{dref} \right) + \tilde{i}_d \left(\frac{1}{\gamma_1} \left(-\frac{R}{L} \hat{i}_d + \omega \hat{i}_q + \frac{E_d}{L} \frac{v_d}{L} - \dot{\tilde{i}}_d \right) \right. \\ & \left. - \omega e_q - \frac{E_d R}{E_q L} \right) + \tilde{i}_q \left(\frac{1}{\gamma_2} \left(-\frac{R}{L} \hat{i}_q - \omega \hat{i}_d + \frac{E_q}{L} \frac{v_q}{L} - \dot{\tilde{i}}_q \right) \right. \\ & \left. + \omega e_d - \frac{R}{L} e_q + \frac{E_d}{E_q} \omega \right) \quad (17) \end{aligned}$$

At last in order to make the derivative of the complete Lyapunov function (16) is negative definite, the direct axis voltage control input and the voltage adaptation law are chosen as follows:

$$\begin{aligned} v_q = & L \left(k_3 e_q + \frac{2k_2}{3Cv_{dc}} E_q e - \frac{R}{L} \hat{i}_q - \left(\omega + \frac{E_d R}{E_q L} \right) \hat{i}_d \right. \\ & + \frac{E_q}{L} - \frac{E_d}{E_q} k_1 e_d - \frac{3}{E_q R_L} v_{dref} \dot{v}_{dref} - \frac{3C}{2E_q} \dot{v}_{dref}^2 \\ & \left. - \frac{3C}{2E_q} v_{dref} \ddot{v}_{dref} \right) \quad (18) \end{aligned}$$

$$\hat{i}_d = \frac{L}{(L+R)} \left(\omega \hat{i}_q + \frac{E_d}{L} \frac{v_d}{L} - \left(\omega e_q + \frac{E_d R}{E_q L} \right) \gamma_1 \right) \quad (19)$$

$$\hat{i}_q = \frac{L}{(L+R)} \left(-\omega \hat{i}_d + \frac{E_q}{L} \frac{v_q}{L} + \left(\omega e_d - \frac{R}{L} e_q + \frac{E_d}{E_q} \omega \right) \gamma_2 \right) \quad (20)$$

Therefore, substituting (18), (19) and (20) into (17), we are able to obtain

$$\dot{V} = - \left(k_1 + \frac{R}{L} \right) e_d^2 - \frac{k_2}{CR_L} e^2 - k_3 e_q^2 - \frac{R}{\gamma_1 L} \tilde{i}_d^2 - \frac{R}{\gamma_2 L} \tilde{i}_q^2 \quad (21)$$

Clearly, \dot{V} in (21) is negative definite, so it implies that the resulting closed loop system is asymptotically stable and, hence, all the error variables e_d, e and e_q and the estimation error \tilde{i}_d, \tilde{i}_q will converge to zero asymptotically. Therefore, the d -axis current \hat{i}_d will converge to zero and the DC voltage will converge to the reference output voltage for PWM AC-DC converter. In addition, because e_d and e_q can converge to zero, the estimated input current for AC-DC converter will converge to the actual real input current in the primary side of the converter.

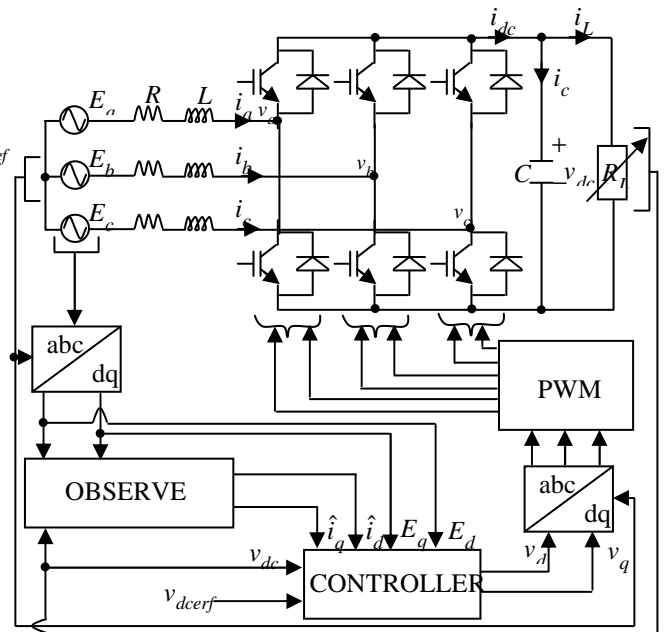


Fig. 2 The block diagram of AC current tracking design scheme

4. Simulation results

In our simulations, the system parameters and design constants of the PWM AC-DC converter are given in Tables I and II, respectively.

Table I

PWM AC-DC Converter system parameters

Supply's voltage and frequency	220V,50Hz
Line's inductor and resistance	0.1mH,2m Ω
DC link resistance	20 Ω
Output capacitors	370 μ F
PWM carrier frequency	1kHz

Table II
Control Design Constants

k_1	k_2	k_3	γ_1	γ_2
10000	1	70	1	1

The voltage transient responses are chosen in the following two cases:

Case I: DC voltage reference changes from 250V to 350V and back to 250V.

Case II: load changes from 20 Ω to 10 Ω .

The sensorless backstepping control with AC current observer is used in our comparative simulations. Fig.3 and 4 illustrate respectively the simulation results of voltage transient responses for DC voltage reference and for load changes.

The fig.3a and fig.4a show the voltage tracking response of the reference and real output DC voltage in the case of DC voltage reference and load changes and the error between them.

The fig.3b and fig.4b show the supply voltage and current in the primary side, the AC current estimated i_d and i_q and the error between the estimated currents and reference currents in cases I and II, respectively.

It can be seen from fig.3a and fig.4a that the actual DC voltage responses can track the reference DC voltage in the beginning when the reference DC voltage is an exponential lick signal. From the DC voltage tracking simulation results in fig.3b and fig.4b, we can find that the estimated AC current with backstepping controllers have excellent performances.

The error between real DC voltage and reference DC voltage can converge to zero asymptotically. Therefore, the estimated AC current are able to converge to the references current. The direct axis current i_d estimated is always forced to zero in order to correct and maintain to unity the primary power factor between source current and source voltage. The q axis current i_q estimated will approach a constant when the actual DC voltage reaches the reference DC voltage. Finally, we can find that the (d,q) axis current errors between estimated ones and the references can converge to zero asymptotically.

From the comparison of DC voltage tracking simulation results shown in fig.3b and fig.4b, it is clear that the estimated AC current with backstepping controllers have excellent performances. The direct axis current estimated is always forced to be zero as we have already expected.

From the simulation results, it is obvious that the proposed sensorless backstepping controllers are quite successful and efficient for the PWM AC-DC converter current tracking control.

5. Conclusion

In this paper, we have implemented and simulated the backstepping control and AC current observer which provide an efficient control design for both tracking and regulation. The resulting closed-loop system is guaranteed to be asymptotically stable, and the AC current tracking error and the AC current estimation error are able to converge to zero asymptotically according to Lyapunov stability theorem. In addition the simulation results have clearly illustrated that the proposed nonlinear backstepping controllers are quite effective and efficient for the PWM AC-DC converter current tracking control without the need of current sensors. The strategy control was very robust to uncertain parameters and gave a very high power factor and small ripple in the current line supply.

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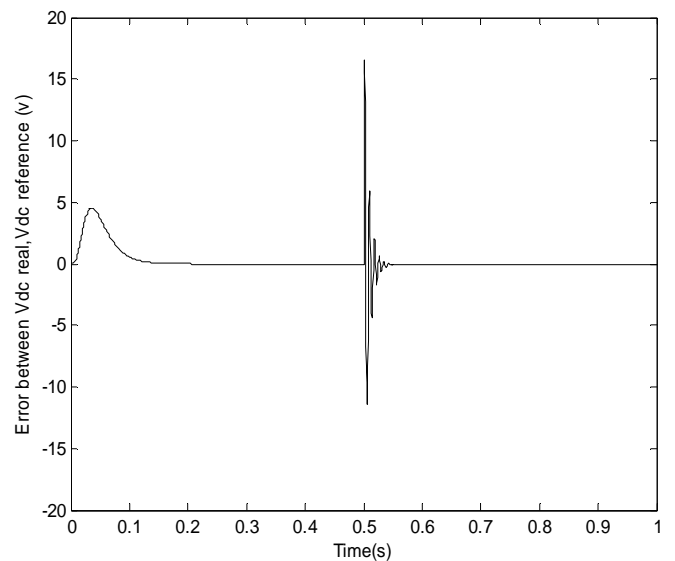
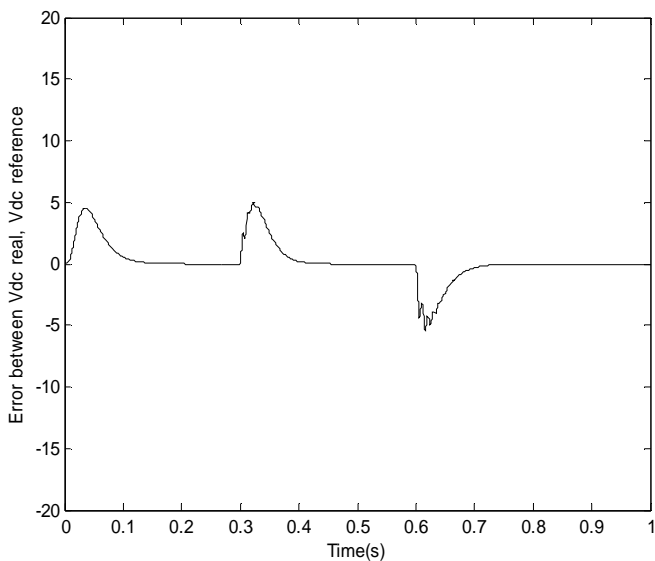
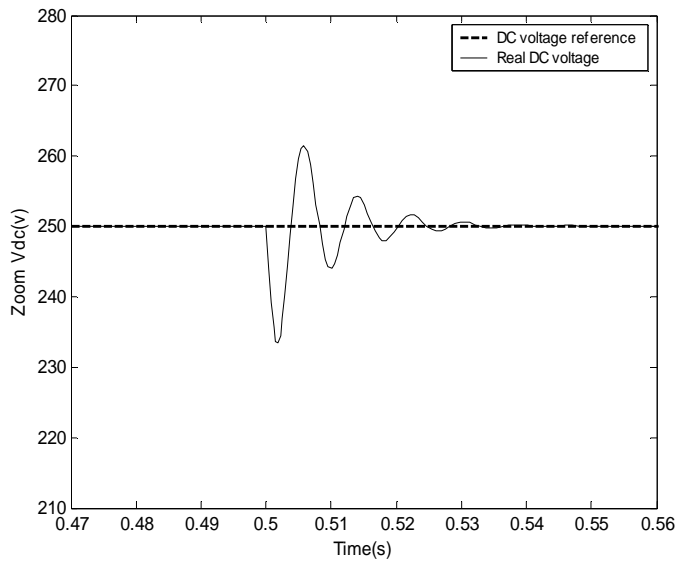
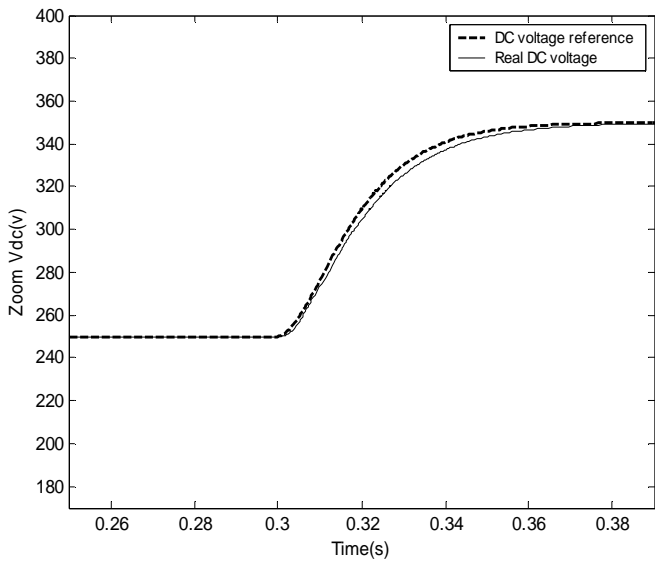
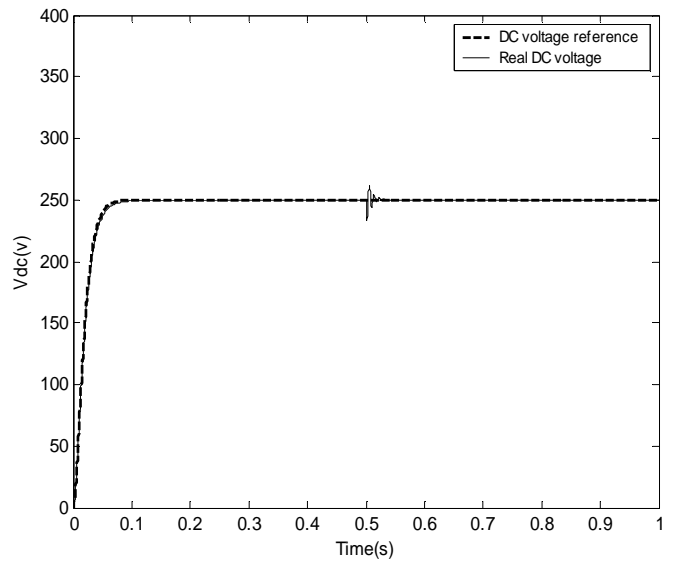
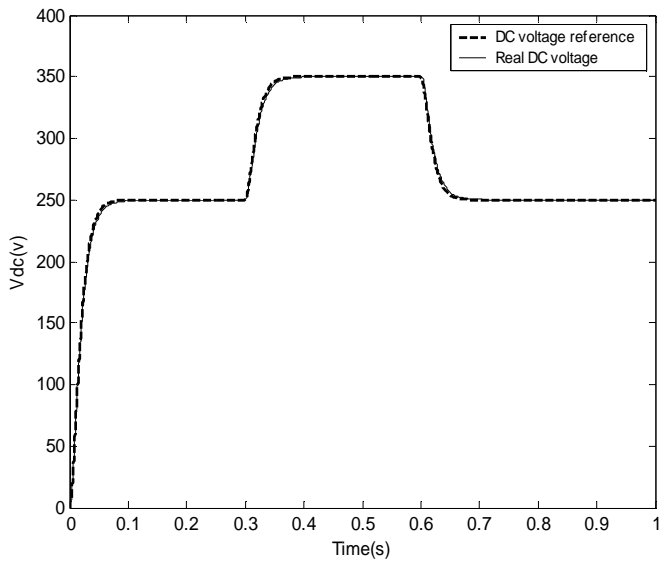


Fig. 3.a Voltage transient responses for dc-voltage reference changes

Fig. 4.a Voltage transient responses for load changes

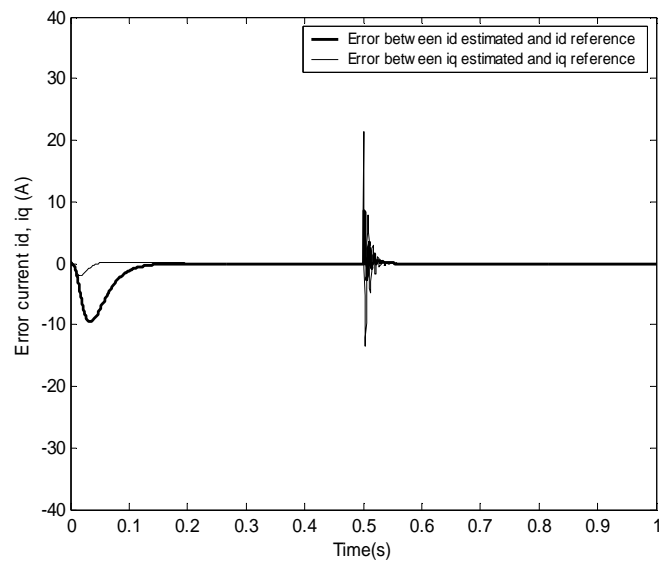
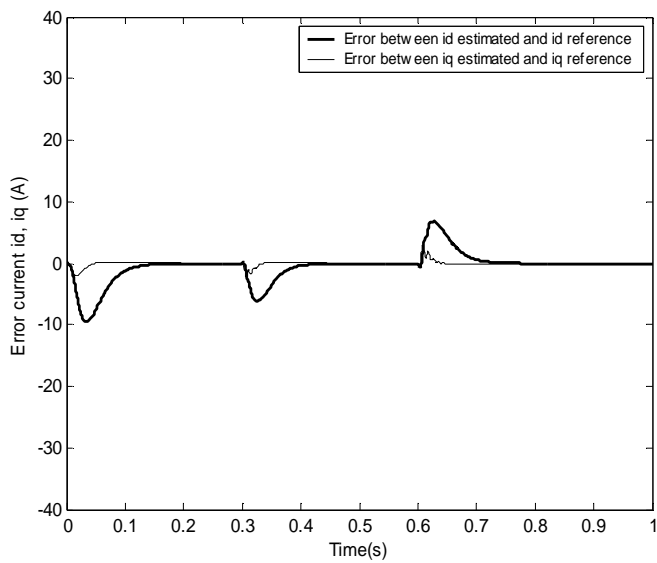
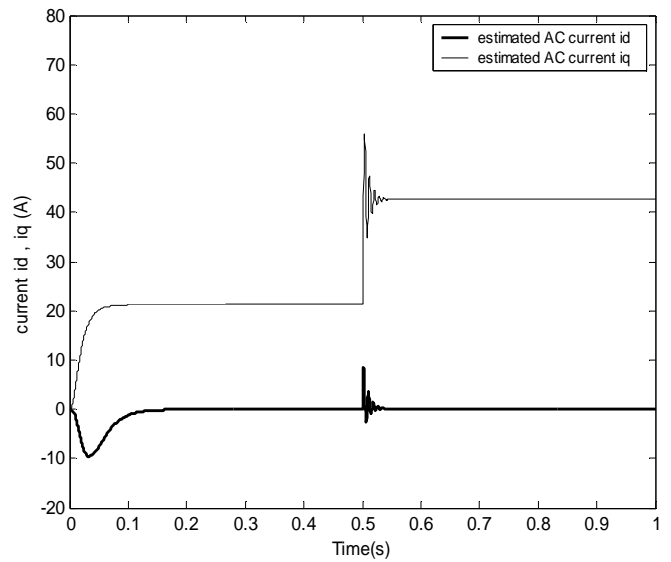
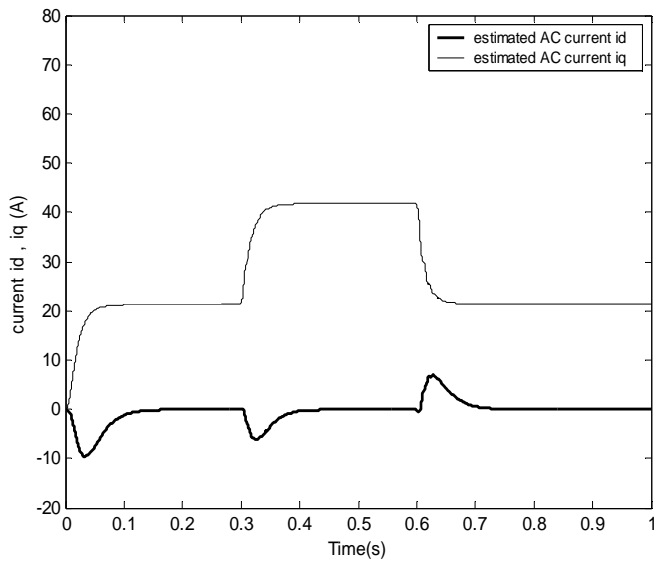
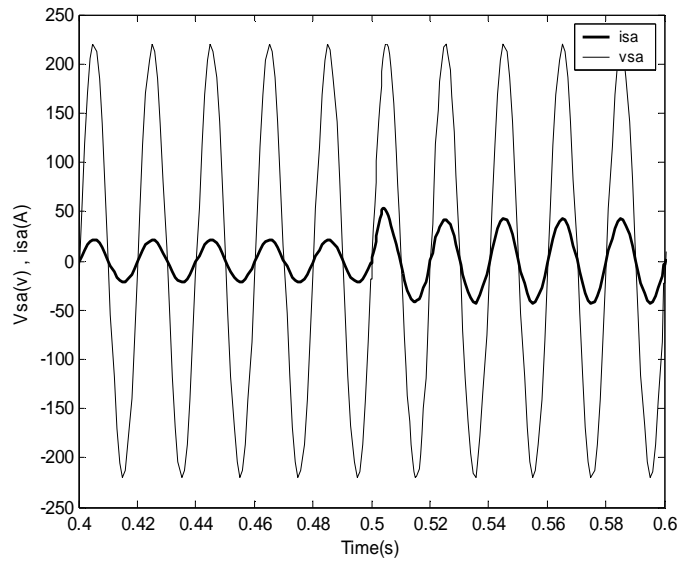
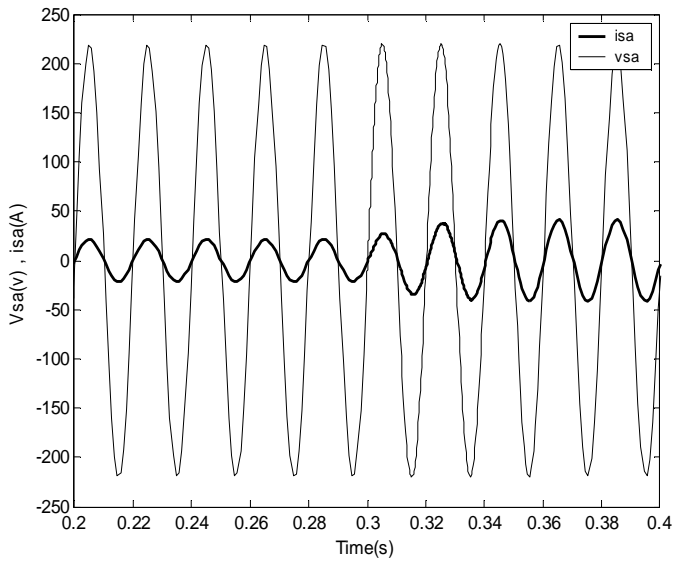


Fig. 3.b Voltage transient responses for dc-voltage reference changes

Fig. 4.b Voltage transient responses for load changes