

New Space-Vector Hysteresis Current Control with Neutral Point Voltage Balancing Applied on Three-Level VSI for Wind Power Conversion

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Abstract. This paper presents a new space-vector hysteresis current control (SV-HCC) with neutral point voltage balancing applied on a three-level NPC-VSI. The last supplies a rotor winding of the doubly fed induction generator (DFIG). The proposed control technique controls the power delivered from the wind generator to the electrical network by controlling the rotor currents of the DFIG. It is based firstly on the detection of area and sector in which the error vector tip of the rotor current can be located, secondly, on the neutral voltage sensing. And then on the application of an appropriate voltage vector among the 27 voltage vectors of the three-level NPC-VSI to accomplish two main goals:
1- Controlling the active and reactive powers delivered by the wind generator to the electrical network.
2- Balancing the neutral point voltage of the three-level VSI.

Key words

Space-vector hysteresis current control, active and reactive powers control, three-level NPC-VSI, neutral point voltage balancing, wind power conversion.

1. Introduction

Wind energy is becoming one of the most important renewable energy sources in European countries, US, and Asia. Over the past decade, new wind generators of 1-3 MW have been developed and are being installed worldwide [1] and prototypes of higher capacity are under development.

As a result, multilevel PWM converters are being increasingly preferred for high power applications such as wind power conversion.

In order to control these converters, many control strategies were proposed. At present, carrier-based modulation (CBM) and space-vector modulation (SVM) [2] have been considered as the most popular modulation strategies for multilevel inverters [3].

However, these control schemes is preferred for voltage control.

The hysteresis controllers are widely used for current control due to their simplicity and robustness [4]. However the phase current interactions, generation of sub-harmonic components in the current and switching losses have been considered as the main drawbacks of the conventional hysteresis controllers [5, 6].

In this paper, a new space-vector hysteresis current control (SV-HCC) is presented for three-level NPC-VSI to accomplish the two following tasks:

- 1- Controlling the active and reactive powers delivered by the wind generator to the electrical network.
- 2- Balancing the neutral point voltage of the three-level VSI.

The present paper focuses only on the rotor side converter control of the DFIG using the proposed SV-HCC.

2. Power system description

The power system based wind power generation is illustrated in figure 1. It consists of a wind turbine, a gearbox, a DFIG, and back-to-back three-level converters. The stator winding of the DFIG is directly connected to the grid, whereas its rotor winding is fed by back-to-back three-level PWM converters. The line side converter is connected to the grid via three chokes to improve the current harmonic distortion. The rotor side converter controls the power flow to the network by controlling the rotor currents of the DFIG.

3. Dynamic model of the DFIG

The electric equations in Park reference frame are given as follows:

$$\begin{cases} v_{sd} = R_s i_{sd} + \frac{d\phi_{sd}}{dt} - \omega_s \phi_{sq} \\ v_{sq} = R_s i_{sq} + \frac{d\phi_{sq}}{dt} + \omega_s \phi_{sd} \end{cases} \quad (1)$$

$$\begin{cases} v_{rd} = R_r i_{rd} + \frac{d\phi_{rd}}{dt} - \omega_r \phi_{rq} \\ v_{rq} = R_r i_{rq} + \frac{d\phi_{rq}}{dt} + \omega_r \phi_{rd} \end{cases} \quad (2)$$

$$\begin{cases} P_s = -v_s \frac{L_m}{L_s} i_{rq} \\ Q_s = \frac{v_s \phi_s}{L_s} - \frac{v_s L_m}{L_s} i_{rd} \end{cases} \quad (3)$$

The block diagram of the DFIG is depicted in figure2.

After being developed the electrical equations in the stationary reference frame [7, 8], the electrical power equations are given below:

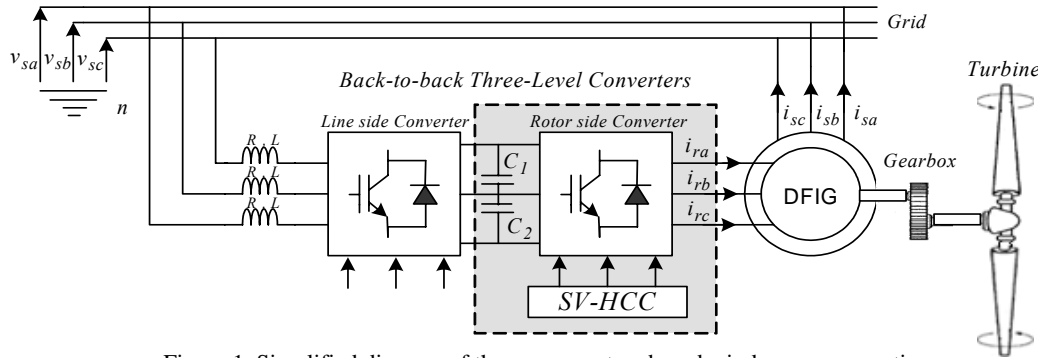


Figure.1. Simplified diagram of the power system based wind power generation

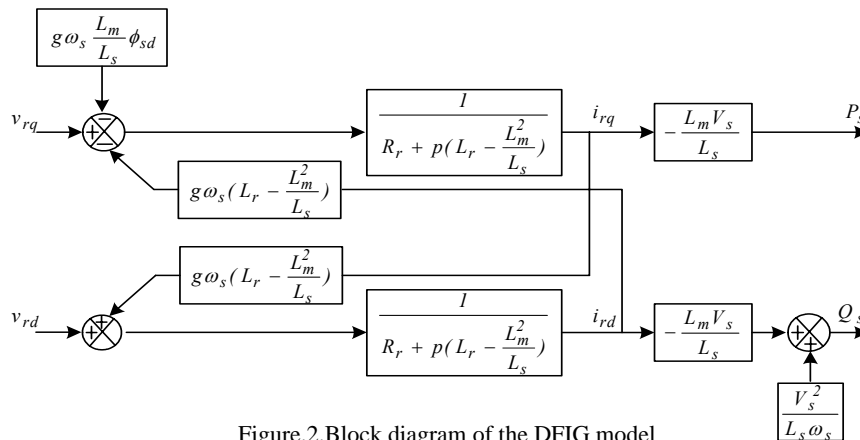


Figure.2. Block diagram of the DFIG model

4. Space-vector hysteresis current control

A. Overall control system

The block diagram of the overall control system is shown in figure 3. The active and reactive powers of the DFIG are controlled by means of controlling the direct and quadrature components of its rotor currents. To accomplish this task, two PI controllers and a SV-HCC technique are used.

B. Principle of the space- vector hysteresis current control

The basic idea of the proposed SV-HCC is to keep the measured rotor currents of the DFIG within their hysteresis boundaries h_1, h_2, h_3 all the time. The reference and the measured rotor currents are transformed to α, β reference frame. They can be expressed as follows:

$$\begin{cases} \vec{i}_r^* = \vec{i}_{r\alpha}^* + j \vec{i}_{r\beta}^* \\ \vec{i}_r = \vec{i}_{r\alpha} + j \vec{i}_{r\beta} \end{cases} \quad (4)$$

By the same way, the error vector defined by $\vec{e} = \vec{i}_r^* - \vec{i}_r$, can be given in $\alpha\beta$ reference frame by the following expression:

$$\vec{e} = \vec{e}_\alpha + j \vec{e}_\beta \quad (5)$$

As it is illustrated in figure 4.a, the tip of the reference current vector \vec{i}_r^* is located in the center of area I whereas the tip of the measured current \vec{i}_r can be located in one of the four areas (I, II, III and IV) limited by the hysteresis

boundaries h_1, h_2, h_3 respectively. Table I gives the membership conditions of the error vector tip in the four possible areas. Each one of the three areas II, III and IV is divided into four sectors and area I represents one sector, which is sector (S_5). The sectors are numbered from 1 to 13. Figure 4.b shows an example of one sector of each area. Table II gives the membership conditions of the error vector tip to the thirteen possible sectors.

TABLE II
THE MEMBER SHIP CONDITIONS OF THE ERROR VECTOR TIP TO THE THIRTEEN SECTORS

Area e_α e_β	A _I	A _{II}	A _{III}	A _{IV}
- -	S ₅	S ₁	S ₆	S ₁₀
+ -	S ₅	S ₂	S ₇	S ₁₁
- +	S ₅	S ₃	S ₈	S ₁₂
+ +	S ₅	S ₄	S ₉	S ₁₃

TABLE I
THE MEMBER SHIP CONDITIONS OF THE ERROR VECTOR TIP TO THE FOURTH AREAS

Conditions	Areas
$ e_\alpha < h_1/2$ & $ e_\beta < h_1/2$	I
$h_1/2 < e_\alpha < h_2/2$ & $h_1/2 < e_\beta < h_2/2$	II
$h_2/2 < e_\alpha < h_3/2$ & $h_2/2 < e_\beta < h_3/2$	III
$ e_\alpha > h_3/2$ & $ e_\beta > h_3/2$	IV

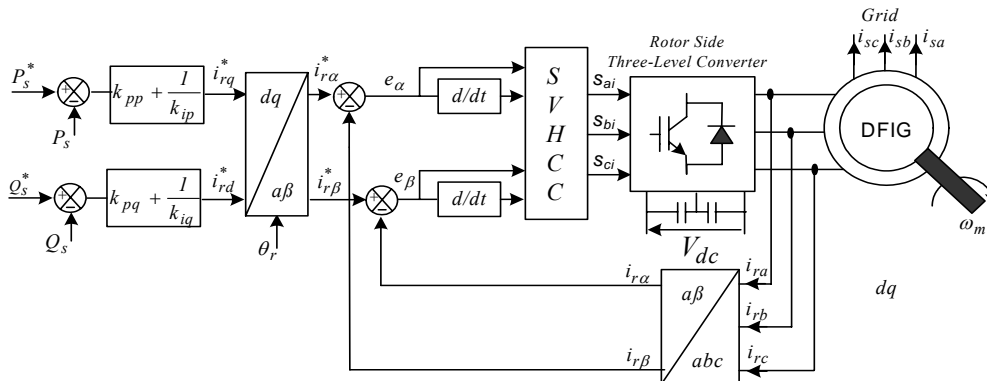


Figure.3. Block diagram of the overall control system

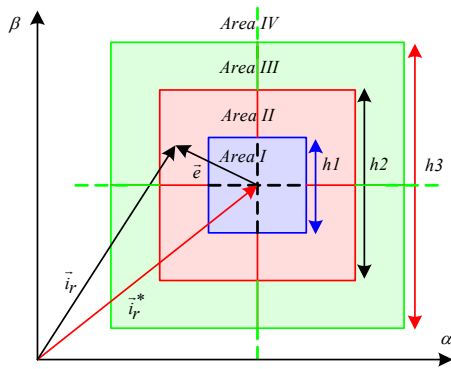


Figure 4.a

Figure.4. Error vector tip location in the four possible areas and in the thirteen possible sectors

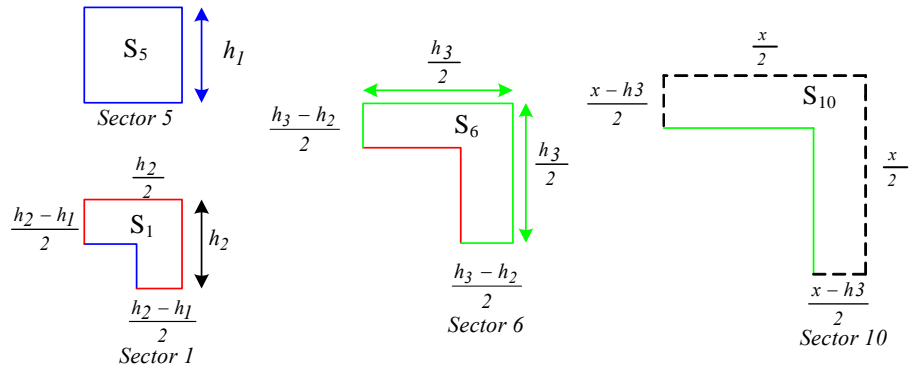


Figure 4.b

C. Neutral point voltage balancing

When zero voltage is applied to any phase, the sum of the currents flowing through the clamping diodes, called i_{NP} , produces an unbalanced DC capacitors charging process. By

this way, a positive current i_{NP} from the inverter produces a charge in the upper capacitor (C_1), and a negative current produces a discharge in the same capacitor [9] (figure 5).

A neutral Point unbalance appears when $\Delta v = v_{c1} - v_{c2} \neq 0$ or $i_{NP} \neq 0$.

The 27 voltage vectors shown in figure 6 are divided into three categories according to their lengths: large, medium and small. Only the small vectors have two realisations: one positive and one negative switching function. The positive one ($\vec{v}_{21}, \vec{v}_{22}, \vec{v}_{23}, \vec{v}_{24}, \vec{v}_{25}, \vec{v}_{26}$) charges the upper capacitor C_1 , whereas the negative one ($\vec{v}_1, \vec{v}_2, \vec{v}_3, \vec{v}_4, \vec{v}_5, \vec{v}_6$) discharges it. The neutral point voltage balancing is achieved by sensing the neutral point voltage Δv , and according to its sign one among the 12 small vectors (positive or negative ones) is applied to charge or discharge the upper capacitor C_1 .

Once the sector is detected, the error derivative is calculated and the neutral point voltage is sensed, an appropriate voltage vector among the 27 voltage vectors shown in figure 6 is applied.

One among the three zero voltage vectors (ZVV) ($\vec{v}_0, \vec{v}_7, \vec{v}_{14}$) is applied when the error vector tip is located in area I.

In the case where the error vector tip is located in area II, one among the small hexagon voltage vectors (figure 6.c) will be applied. By the same way, if the error vector tip is located in area III, one among of the medium hexagon voltage vectors (figure 6.b) will be selected. Otherwise, one of the large hexagon voltage vectors (figure 6.a) will be applied when the error vector tip is located in the area IV (outside areas I, II and III).

Therefore a simple look-up table (table III), which uses the sector information, the error derivative sign and the NP voltage sign, is developed to satisfy the following two conditions at the same time:

- 1- Controlling the active and reactive powers of the DFIG by controlling its rotor currents.
- 2- Balancing the neutral point voltage.

Figure 7 shows the control algorithm of the SV-HCC including the neutral point voltage balancing. The hatched part of this algorithm, which is responsible of the neutral point voltage balancing, is involved only when the vector tip of current error \vec{e} is located in area II. Otherwise, there is no need to this part.

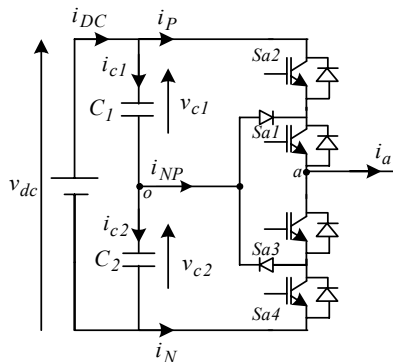


Figure.5. Neutral point voltage circuit

TABLE III
SWITCHING TABLE FOR THE PROPOSED S.V.H.C.C

$\frac{de_\alpha}{dt}$	$\frac{de_\beta}{dt}$	S ₁	S ₂	S ₃	S ₄	S ₅	S ₆	S ₇	S ₈	S ₉	S ₁₀	S ₁₁	S ₁₂	S ₁₃
0	0	V ₂ or V ₂₂	V ₃ or V ₂₃	V ₁ or V ₂₁	ZVV	ZVV	V ₈	V ₉	V ₁₃	ZVV	V ₁₆	V ₁₇	V ₁₅	ZVV
1	0	V ₂ or V ₂₂	V ₃ or V ₂₃	ZVV	V ₄ or V ₂₄	ZVV	V ₉	V ₁₀	ZVV	V ₁₁	V ₁₆	V ₁₇	ZVV	V ₁₈
0	1	V ₁ or V ₂₁	ZVV	V ₆ or V ₂₆	V ₅ or V ₂₅	ZVV	V ₈	ZVV	V ₁₃	V ₁₂	V ₁₅	ZVV	V ₂₀	V ₁₉
1	1	ZVV	V ₄ or V ₂₄	V ₆ or V ₂₆	V ₅ or V ₂₅	ZVV	ZVV	V ₁₀	V ₁₂	V ₁₁	ZVV	V ₁₈	V ₂₀	V ₁₉

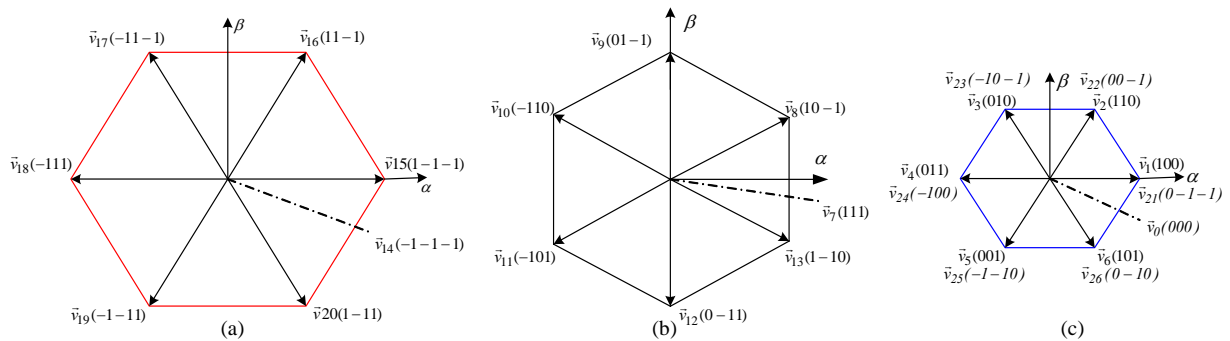


Figure.6. Three-level inverter vectors in $\alpha\beta$ plane

- (a) Large hexagon.
- (b) Medium hexagon.
- (c) Small hexagon.

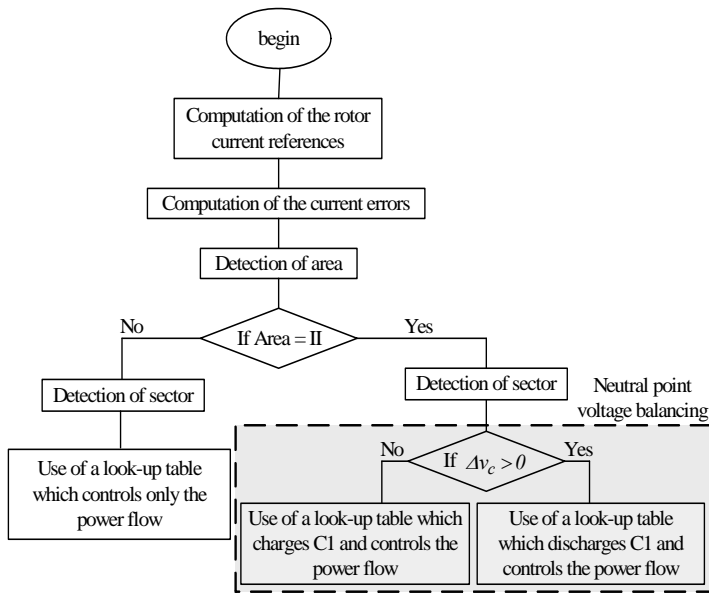


Figure.7. Control algorithm of the SV-HCC including the neutral point voltage balancing

5. Simulation results

The proposed SV-HCC technique with neutral point voltage control was tested on three-phase three-level VSI, which controls the active and reactive powers of the DFIG by controlling its rotor currents. The simulation of the overall system shown in figure 1 has been carried out to verify the performances of the proposed control technique. All simulation results are obtained with the following parameters:

-The simulation parameters of the two PI controllers used for active and reactive powers control are: $K_{ip} = 2.5$, $K_{pp} = 0.02$, $K_{iq} = 2.5$, $K_{pq} = 0.02$.

-The hysteresis bands of the proposed SV-HCC are fixed at $(h_1 = 0.445 A, h_2 = 0.47 A, h_3 = 0.495 A)$.

-The speed of the DFIG is fixed at $(\omega_m = 1624 rpmn)$, and the dc capacitors are fixed at $C_1 = C_2 = 25 mF$.

- The DFIG parameters are:

Nominal power ($P=7.5KW$), nominal speed ($N=1500 rpmn$), pairs of pole number ($p=2$), Inertial ($J=0.3125Kg.m^2$), viscous coefficient ($f=6.73.10e^{-3}N.m.s^{-1}$), stator resistance ($R_s=0.455 \Omega$), Stator inductance ($L_s=0.084H$), Rotor resistance ($R_r=0.062 \Omega$), rotor inductance ($L_r=0.081H$), main inductance ($L_m=0.078$).

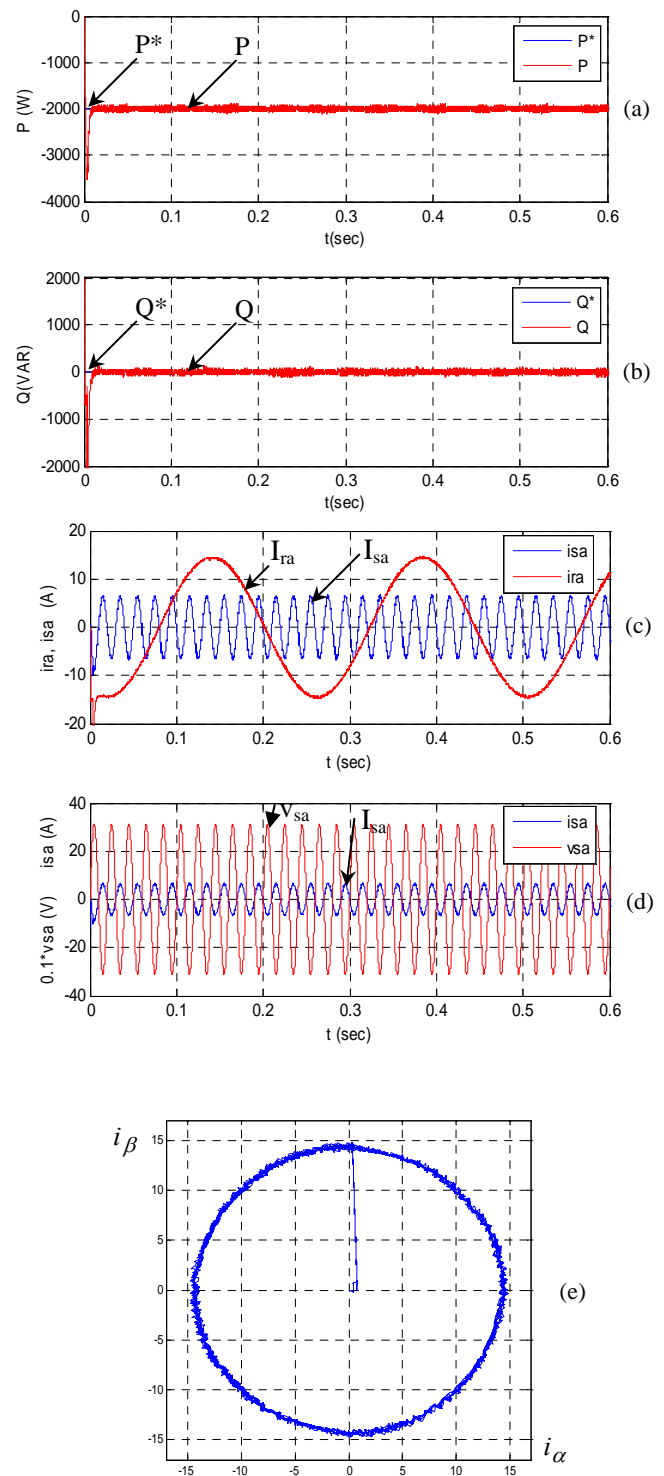


Figure.8. Simulation waveforms of the SV-HCC applied on the rotor side three-level converter

- (a) Reference and measured active powers of the DFIG.
- (b) Reference and measured reactive powers of the DFIG.
- (c) Rotor and stator currents of the DFIG.
- (d) Supply voltage and stator current of the DFIG.
- (e) The trajectory of the rotor current of the DFIG.

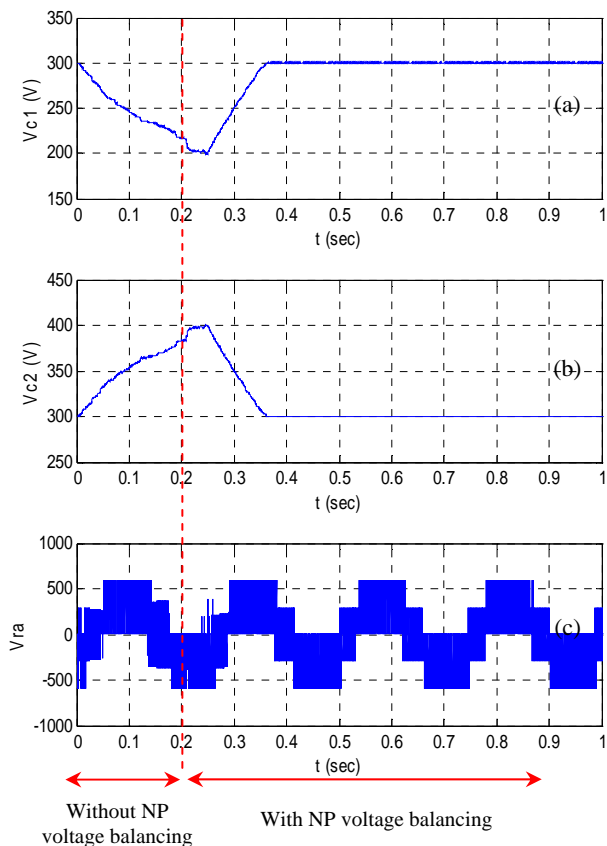


Figure.9.Simulation waveforms of the neutral point voltage control

- (a) Voltage across the upper capacitor C_1 .
- (b) Voltage across the lower capacitor C_2 .
- (c) Phase to phase output voltage of the three-level VSI

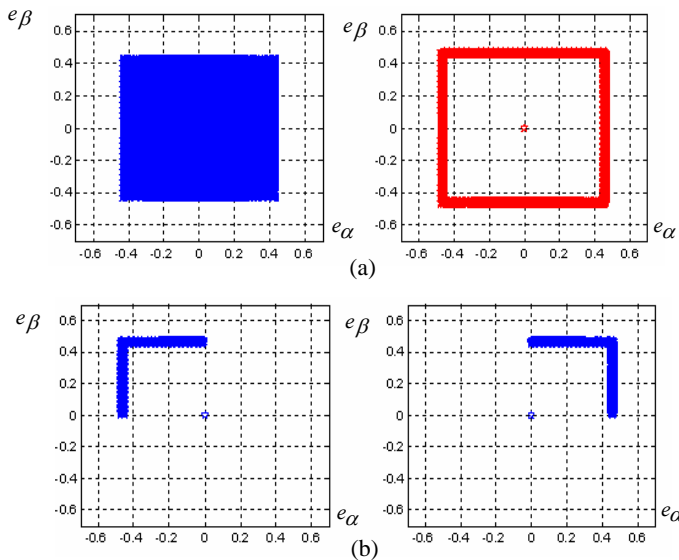


Figure.10. Current error movement in α, β plane
 (a) Current error movement in areas I and II
 (b) Current error movement in sectors S_1 and S_2 of area II

Figure 8 illustrates the simulation waveforms of the rotor side three-level converter control shown in figure 1.e. It is clearly seen from figures 8.a and 8.b, that the active and reactive powers of the DFIG follow respectively their references

The stator and rotor currents of the DFIG are shown in figure 8.c. It is obvious that the rotor current frequency is lower than the stator current frequency. It is clear from figure 8.d, that the stator current and the supply voltage are in π phase; hence the DFIG injects an active current to the grid. The rotor current trajectory of the DFIG is illustrated in figure 8.e. It is clearly denoted that it is circular in α, β plane.

The algorithm of the proposed scheme without the NP voltage balancing part is applied to the three-level VSI before $t = 0.25$ s. As a result, the upper capacitor C_1 is discharging and lower capacitor C_2 is charging. This is clearly noticed in figure 9.a and 9.b. Hence, the NP voltage unbalance affects the three-level VSI output voltages, as it is shown in figure 9.c. Both voltages across the capacitors v_{c1} and v_{c2} start to converge towards half of the dc voltage (v_{dc}) at $t = 0.25$ s since the neutral point voltage control is involved.

Figure 10.a shows the current error movement in areas (I and II) and figure 10.b illustrates the current error movement in the four sectors of area II.

6. Conclusion

A new space- vector based on hysteresis current control has been used in this paper to control the active and reactive powers delivered from the wind generator to the electrical network. Beside this, the control scheme balances the neutral point voltage using redundant inverter switching states. Moreover, it gathers the three current errors into a single space vector quantity. Hence the magnitude of the error vector is limited within a boundary area of a square shape, and also the phase current interactions are avoided in the three phase system. The simulation results of the overall control system show good performances.

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