

Generalized Predictive Current Control for Grid-Connected Converter with LCL Filter

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Abstract. The current control based on the GPC (Generalized Predictive Control) algorithm for the power electronic converter connected to the grid via LCL filter is developed. A design process of the control structure and its parameters, and the results of simulation are presented. The current responses to changes in current references and to grid failures are shown and discussed. The algorithm works well also with unbalanced grid voltage. The simplicity and low sensitivity of the basic performance criteria to changes of the LCL filter parameters can be viewed as the important benefit of the strategy developed.

Key words. Generalized predictive current control, grid-connected converter, LCL filter, sensitivity to parameter changes, simulation.

1. Introduction

The Model Predictive Control (MPC) has become popular also in the controllers used in power electronics applications [1]-[5]. A special type of the MPC is the Generalized Predictive Control (GPC). The GPC makes it possible to work with long prediction and/or control horizons [6]. An input-variation term which influences control dynamics is also a part of the GPC. The GPC has been recently used also in the field of the control of power electronic converters.

The current control of the converter with L grid filter using the Generalized Predictive Current Control (GPCC) was developed and tested in [7]-[9]. The power control based on the GPCC applied in the grid-connected converter with L filter was presented in [10].

But, the simplest L filter has low efficiency for suppression of current harmonics. That is the main reason why a more efficient LCL filter is used.

The grid filter structure can be developed according to an acceptable grid current spectrum defined by the IEEE 519-2014 standard in the USA and IEC/TR 61000-3-6:2008 in Europe.

An essential parameter is the rate of the short circuit power at a PCC (Point of Common Coupling) and the nominal power of the converter connected to the PCC. In contribution [11] a possible procedure for the LCL filter design was presented.

Nevertheless, when the full LCL filter topology is considered, it brings some difficulties in the design of

current control circuitry. Different control and PWM (Pulse Width Modulation) strategies can be used. Each possible control and PWM strategy has some advantages, but also some disadvantages and limits, [12]-[15]. Among all methods, also the MPC theory has found its application here [16]-[18].

The presented contribution presents an application of the GPCC that has been applied in this area only rarely. Especially, the GPCC applied for the grid-connected voltage source inverter with the LCL filter was presented in [19] for the first time. It was found that the GPCC strategy can be used without the need for any model-based active damping strategy and speed and robustness against model mismatch can be achieved with low computational burden.

Taking into account previous experience in this area, the contribution is focused on using a rather different strategy of generation of the GPCC algorithm than the one presented in [19].

The presented GPCC with short settling times is tested for current control in a voltage converter connected to the grid via LCL filter. A design procedure and results of simulation tests of the GPCC are presented. The presented strategy works well even for unbalanced grid voltages.

The paper is organized as follows: section 2 presents the block diagram of a grid-connected converter and control system; section 3 sets out the equations describing the LCL grid filter between the converter and grid; section 4 summarizes the basic principles of the generalized predictive current control; section 5 presents simulation results for grid-connected converter with the LCL filter, which is controlled by the GPCC strategy; and the conclusions are summarized in the final section 6.

2. Block diagram of grid-connected converter and control system

Figure 1 shows the block diagram of the PWM converter connected to the grid via the LCL filter and with the control of the grid current i_{L2} . The reference current components i_{L2d}^* , i_{L2q}^* are transformed to the $\alpha\beta$ static reference frame in which the GPCC is performed as it is shown in section 4. The output converter voltage v_1 is generated applying PWM. The controller of the DC voltage v_c is not included in the following analysis and simulation.

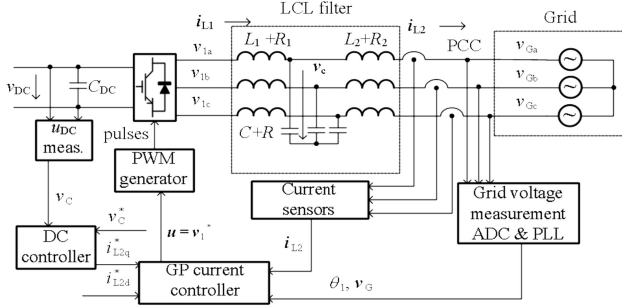


Fig. 1. Block diagram of PWM converter connected to grid via LCL filter and with GPCC of i_{L2} .

3. Equations describing LCL grid filter between converter and grid

Contrary to [10] the LCL type of the filter between the converter and the grid is considered here. It is known that for the resonant frequency of the LCL filter ω_{REZ} (its recommended value is $10 \omega_1 < \omega_{REZ} < \omega_{SW}/2$ where ω_{SW} is switching frequency of the converter) the impedance of the filter is zero and harmonic currents near this frequency are amplified. This effect can be mitigated if some dumping resistances are included into the LCL filter branches. Also the concept of virtual dumping resistances is being used, which results in a control circuitry modification. But, additional sensors must be used in such a case. Thus, the developed GPCC algorithm takes into consideration the resistances in all branches of the LCL filter.

The model of the LCL filter in the Laplace domain is used at the start. After that this model will be transformed into the Z-domain using the Euler backward approximation of the original model in the Laplace domain.

The construction of the model in the Laplace domain is divided into two parts: in the first one the grid voltage v_g is supposed to be zero ($v_g = 0$), in the second one zero output voltage of the converter $v_1 = 0$.

First part ($v_g = 0$):

The parallel combination of capacitor C and inductance L_2 including also respective resistances can be characterized in the Laplace domain as

$$Z_{2C}(s) = \frac{s^2 R C L_2 + s(L_2 + R R_2 C) + R_2}{s^2 C L_2 + s(R_2 + R)C + 1} \quad (1)$$

Considering series connection Z_{2C} and inductance L_2 the following equation can be written for the transfer function $i_{L2}(s)/v_1(s)$

$$\frac{i_{L2}(s)}{v_1(s)} = \frac{B_1(s)}{A_1(s)} = \frac{Z_{2C}}{(R_2 + sL_2)[(R_1 + sL_1) + Z_{2C}]} \quad (2)$$

Second part ($v_1 = 0$):

The parallel combination of capacitor C and inductance L_1 including also respective resistances can be characterized in the Laplace domain as

$$Z_{1C}(s) = \frac{s^2 R C L_1 + s(L_1 + R R_1 C) + R_1}{s^2 C L_1 + s(R_1 + R)C + 1} \quad (3)$$

Considering series connection Z_{1C} and inductance L_2 the following equation can be written for the transfer function $i_{L2}(s)/v_G(s)$

$$\frac{i_{L2}(s)}{v_G(s)} = \frac{B_G(s)}{A_G(s)} = -\frac{1}{(R_2 + sL_2) + Z_{1C}} \quad (4)$$

Therefore the following equation can be written for the current $i_{L2}(s)$ after some manipulations and in a shortened form

$$i_{L2}(s) = v_1(s) \frac{b_{21}s^2 + b_{11}s + b_{01}}{a_{41}s^4 + a_{31}s^3 + a_{21}s^2 + a_{11}s + a_{01}} - v_G(s) \frac{b_{2G}s^2 + b_{1G}s + b_{0G}}{a_{3G}s^3 + a_{2G}s^2 + a_{1G}s + a_{0G}} \quad (5)$$

with input variables $v_1(s)$ and $v_G(s)$.

To find the algorithm for the GP current controller in both the axes $\alpha\beta$ of the static reference frame transition to the Z-domain is necessary. For example the Euler backward approximation can be used

$$s \triangleq \frac{1 - z^{-1}}{T_s} \quad (6)$$

Doing that the following equations can be formulated where the coefficients in numerators and denominators are calculated from the respective coefficients of (5)

$$\frac{i_{L2}(z^{-1})}{v_1(z^{-1})} = H_1(z^{-1}) = \frac{B_1(z^{-1})}{A_1(z^{-1})} = \mathbb{Z} \left\{ \frac{B_1(s)}{A_1(s)} \right\} = \frac{B_{01} + B_{11}z^{-1} + B_{21}z^{-2}}{A_{01} + A_{11}z^{-1} + A_{21}z^{-2} + A_{31}z^{-3} + A_{41}z^{-4}} \quad (7)$$

and

$$\frac{i_{L2}(z^{-1})}{v_G(z^{-1})} = H_G(z^{-1}) = \frac{B_G(z^{-1})}{A_G(z^{-1})} = \mathbb{Z} \left\{ \frac{B_G(s)}{A_G(s)} \right\} = \frac{B_{0G} + B_{1G}z^{-1} + B_{2G}z^{-2}}{A_{0G} + A_{1G}z^{-1} + A_{2G}z^{-2} + A_{3G}z^{-3}} \quad (8)$$

The principle of the GPCC (GP Current Control) of the grid-connected converter will be recapitulated in the following paragraph.

4. Generalized current controller of grid-connected converter.

The GP algorithm will be presented here just in principle because the algorithm was presented in more detail in [7]-[10]. As an example the following equation can be written for the transfer function i_{La}/v_{1a} in the axis a

$$H_{1a}(z^{-1}) = \frac{B_1(z^{-1})}{A_1(z^{-1})} = \frac{i_{L2a}(z^{-1})}{v_{1a}(z^{-1})} = \frac{y(z^{-1})}{u(z^{-1})} \quad (9)$$

where the variable $u(z^{-1})$ is the control action and $y(z^{-1})$ is the output of the controlled system.

The controlled object H_{1a} may be characterized by a Controlled Auto Regressive Integral Moving Average (CARIMA) model [7], [8]

$$A_1(z^{-1})y(t) = B_1(z^{-1})u(t-1) + \frac{C(z^{-1})}{\Delta}v(t) \quad (10)$$

where

$$\Delta = 1 - z^{-1} \quad (11)$$

is the differentiating operator and $v(t)$ is an additive white noise signal. The observer polynomial $C(z^{-1})$ can be written as follows

$$C(z^{-1}) = 1 + c_1(z^{-1}) + c_2(z^{-2}) + \dots \quad (12)$$

The objective function J with the control signal u is for the GPC defined

$$J = \sum_{j=1}^N [\hat{y}(t+j) - y^*(t+j)]^2 + \sum_{j=1}^{N_u} \lambda [\Delta u^2(t-1+j)] \quad (13)$$

where $\hat{y}(t+j)$ are the j -step ahead predictions of the system output calculated in instant t . They are calculated with regard to the differences of control signal Δu , while $y^*(t+j)$ are the future reference outputs of the controlled system. Coefficient N is the prediction horizon, N_u is the control horizon (usually $N_u = 1$), and λ is the positive weight coefficient. The selection of the parameters N , λ and the polynomial $C(z^{-1})$ influences quality of the control process (stability, bandwidth, settling time, overshoot).

The minimum of the cost function J can be found by making the following gradient equal to zero.

$$\frac{\partial J}{\partial \Delta u} = 0 \quad (14)$$

That is the way to find the algorithm for the control signal u generated by the controller. The algorithm for the specific parameters of the LCL filter is given in the following paragraph.

5. Analytical and simulation results.

According to the procedure presented in [11] the design of the LCL filter for the laboratory converter $S_A = 20$ kVA, $V = 400$ V, switching frequency $f_{sw} = 3$ kHz, and for the short circuit power $S_{kQ} = 25$ MVA was performed.

The parameters of the LCL filter for the nominal grid voltage V_{Gn} may be expressed in % related to the base values (p. u. system)

$$L_b = \frac{Z_b}{\omega_1}, C_b = \frac{1}{\omega_1 Z_b}, \text{ where } Z_b = \frac{V_{Gn}^2}{S_A} \quad (15)$$

Designing the LCL filter parameters, the following restrictions and recommendations should be followed:

- $C(\%) < 5\% C_b$ (to limit the capacitance reactive power),
- $L_1 + L_2(\%) < 10\% L_b$ (to limit the voltage drop),
- $10\omega_1 < \omega_{REZ} < \omega_{sw}/2$,
- damping resistor R connected in series with the capacitor C should be so high to make the respective losses acceptable

$$\Delta P_c = \frac{3}{2} R \sum_h |i_{L1}(h) - i_{L2}(h)|^2 \quad (16)$$

The design of the LCL filter does not represent a straightforward, but iterative process. The LCL filter parameters complying basically the criteria mentioned above are listed in TABLE I where also the parameters of the converter, and voltages used in simulation are listed.

Table I. – Parameters of LCL filter and converter

Inductance L_1 of filter (H)	0.005
Resistance R_1 (Ohm)	1
Inductance L_2 of filter (H)	0.002
Resistance R_2 (Ohm)	0.5
Capacitance C (μF)	20
Resistance R (Ohm)	10
Sampling frequency f_s (Hz)	6000
Switching frequency f_{sw} (Hz)	3000
Grid phase voltage V_{RMS} (V)	115
Converter dc bus voltage V_{dc} (V)	400
Delay time $T_d = T_s$ (ms)	1/6

The algorithm parameters N , λ , and the observer polynomial $C(z^{-1})$ should be found in the first step. The simulation of the system shown in Fig. 1 was done in the Matlab™ environment.

Figure 2 presents the relationship among the overshoot Δh_{max} (%) of responses of the current i_a and the coefficients λ and c_2 ($N = 5$, $c_0 = 1$, $c_1 = 0$). The value $N = 5$ was selected as a good compromise enabling easy selection of the values λ and c_2 .

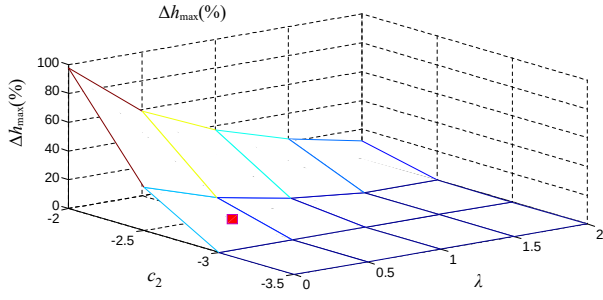


Fig. 2. Overshoot $\Delta h_{\max}$ (%) of current responses for different values of λ and c_2 , $N = 5$. Selected combination of λ and c_2 is marked by small red square.

Figure 3 presents the relationship among the settling time t_s (ms) of the current i_a (the deviation from the set constant value is less than 2 %) and the coefficients λ and c_2 ($N=5$, $c_0=1$, $c_1=0$).

Finally, Fig. 4 presents the relationship among the bandwidth f_{BW} of responses of the current i_a and the coefficients λ and c_2 ($N = 5$, $c_0 = 1$, $c_1 = 0$).

The values $c_2 = -2.7$ and $\lambda = 0.3$ (small red squares in Figs. 2, 3, and 4 indicating the bandwidth $f_{BW} = 716$ Hz, the settling time $t_s = 1.83$ ms, and the overshoot $\Delta h_{\max} = 3.5$ %) were selected for further simulation and investigation as a compromise between the bandwidth and overshoot of the controlled current i_a .

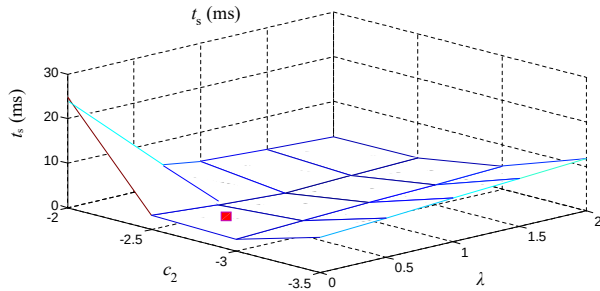


Fig. 3. Settling time t_s (ms) of current responses for different values of λ and c_2 , $N = 5$. Selected combination of λ and c_2 is marked by small red square.

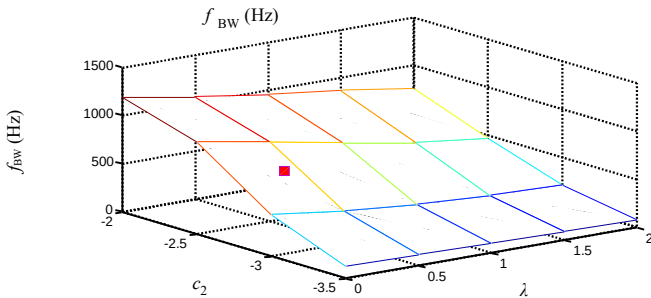


Fig. 4. Bandwidth f_{BW} (Hz) of current responses for different values of λ and c_2 , $N = 5$. Selected combination of λ and c_2 is marked by small red square.

The following expression for the control law $u_a(t)$, considering $N=5$ and the selected parameters c_2 and λ , was obtained (with the assumption that a simple ZOH circuit with a sampling period T_s is used)

$$\begin{aligned} u_a(t) = & 0.8382u_a(t-1) + 0.1624u_a(t-2) - 9.4881e-4u_a(t-3) + \\ & + 3.9611e-4u_a(t-4) - 0.4416i_a(t-1) + 0.0073i_a(t-2) + \\ & + 0.0377i_a(t-3) + 0.2395i_a(t-2) + 0.3015i_a(t+3) + \\ & - 0.3833i_a(t+4) + 0.2389i_a(t+5) \end{aligned} \quad (17)$$

and similar one for the control signal $u_b(t)$. The values i_a^r are the references for the current at future sampling times.

The sensitivity of the basic performance criteria ($\Delta h_{\max}$, t_s and f_{BW}) to changes of the parameters of the LCL filter is illustrated in TABLE II. Except of the values of the performance criteria for the basic parameters these values for deviations of ± 25 % from the nominal parameter values are presented.

Based on this table, following results can be summarized:

- 1) all responses are stable for this extent of parameter errors;
- 2) the sensitivity of all the performance criteria to changes of C , R values is much lower than to changes of the inductances L_1 , L_2 with their resistances R_1 , R_2 ;
- 3) while the performance criteria $\Delta h_{\max}$, t_s are changing for the better with the increasing values of the LCL parameters, the values of the bandwidth f_{BW} become worse for these parameter changes.

Table II. – Sensitivity of performance criteria to changes of selected LCL filter parameters

changes of parameters		-25%	0	+25%
$L_1 + R_1$	$\Delta h_{\max}(\%)$	14	3.5	0
	t_s (ms)	2.33	1.83	1.83
	f_{BW} (Hz)	867	716	565
$L_2 + R_2$	$\Delta h_{\max}(\%)$	8	3.5	0.1
	t_s (ms)	2	1.83	1.8
	f_{BW} (Hz)	796	716	645
$C + R$	$\Delta h_{\max}(\%)$	4	3.5	3
	t_s (ms)	1.83	1.83	1.83
	f_{BW} (Hz)	700	716	724

The following figures were obtained by simulation for the system parameters listed in TABLE I and for the constants $N = 5$, $c_2 = -2.7$ and $\lambda = 0.3$.

Figure 5 shows the responses of the grid and converter voltages and grid currents i_{L2} in both the axes $\alpha\beta$ of the static reference frame after the change from balanced voltage (115-115-115 V_{RMS}) to unbalanced one ($V_{p_{RMS}} = 90$ V, $V_{n_{RMS}} = 25$ V) in $t = 0.06$ s, and after the change of the reference current $i_{L2}^*_{RMS} = 20$ A to 30 A in $t = 0.08$ s. The generated converter voltage components represent here the reference signals for the PWM module. That is why also the presented grid current responses are calculated as if the converter generated such ideal voltages. It is obvious that the responses of the grid

current i_{L2} after both the changes are attenuated very quickly in accordance with the data given in TABLE II.

The following Fig. 6 presents the responses of grid currents i_{L2} in the axes $\alpha\beta$ in detail after the changes of all LCL parameters from the nominal ones to only 60% of their values in $t = 0.07$ s, and after the change of the reference current $i_{L2}^*_{RMS} = 20$ A to 30 A in $t = 0.08$ s. The grid voltage v_G is permanently unbalanced in this simulation. The reference values of both the currents in the axes $\alpha\beta$ are presented as well. It is obvious that the actual responses of the grid current components follow their reference signals with delays about 1 ms or even less and without any serious disturbances that are quickly damped after the mentioned changes. It indicates a good resistance of the presented GPCC algorithm to relatively large parameter changes of the control object.

Finally, Fig. 7 shows the converter voltage and grid current responses in the α axis within one fundamental period and also the frequency spectrum of this current.

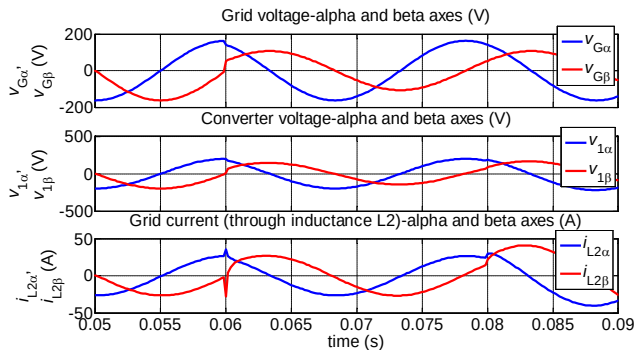


Fig. 5. Responses of grid and converter voltages and grid current i_{L2} in axes $\alpha\beta$ of static reference frame after change from balanced voltage (115-115-115 V_{RMS}) to unbalanced one ($V^p_{RMS} = 90$ V, $V^n_{RMS} = 25$ V) in $t = 0.06$ s, and after change of reference current $i_{L2}^*_{RMS} = 20$ A to 30 A in $t = 0.08$ s.

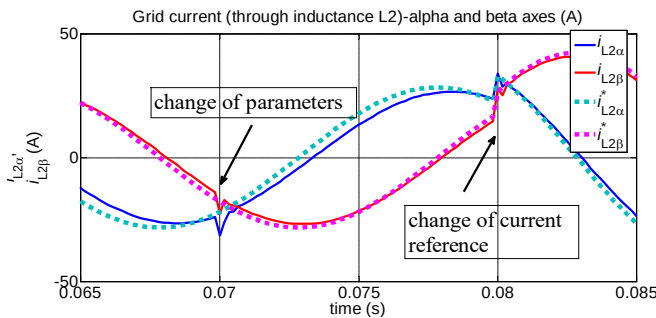


Fig. 6. Responses of grid currents i_{L2} in axes $\alpha\beta$ in detail after changes of all LCL parameters from nominal ones to only 60% of their values in $t = 0.07$ s, and after change of reference current $i_{L2}^*_{RMS} = 20$ A to 30 A in $t = 0.08$ s. Grid voltage v_G is permanently unbalanced

Contrary to the previous two figures the PWM of the converter voltage was assumed. The usual type of modulation, known as the Space Vector Modulation (SVM) was selected. The presented responses are for the balanced grid voltage (115-115-115 V_{RMS}). It is obvious that the highest current harmonic magnitude (the 7th harmonic) is 1.3 % of the magnitude of the fundamental harmonic with frequency 50 Hz. All remaining harmonic components up to 2 kHz are lower than 1 % (except the 11th harmonic they are even lower than 0.5 %).

It is appropriate to recall here that the sampling frequency is only $f_s = 6$ kHz, so the switching frequency is $f_{sw} = 3$ kHz owing to the used SVM algorithm. Using other types of the PWM somewhat different harmonic spectra can be obtained.

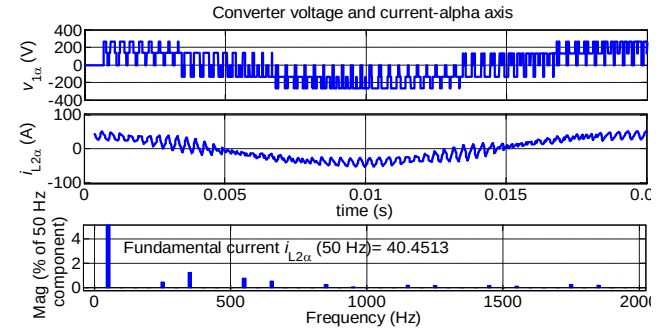


Fig. 7. Converter voltage and grid current responses in α axis within one fundamental period and also frequency spectrum of this current. Responses are for balanced grid voltage (115-115-115 V_{RMS}) and switching frequency $f_{sw} = 3$ kHz (SVM algorithm used).

6. Conclusion

The GPC based current control for the three-phase voltage source converter connected to the grid via LCL filter was developed, simulated, and tested. The prediction horizon N , weight coefficient λ , and polynomial $C(z^{-1})$, which are the main parameters of the GPC control were designed and examined by simulations. The current responses under different grid failures and for current reference changes were presented and evaluated. The algorithm works well also with unbalanced grid voltage. The simplicity and low sensitivity of the basic performance criteria to changes of the LCL filter parameters can be also viewed as an important benefit of the strategy developed.

Acknowledgement

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